

## Device Index

Device group	Target device series
ADAM & SRC (4/8bit SRC MCU)	MC4x series ADAM2x series ADAM4x series ADAM8x series
8051 & 800 (8bit general purpose MCU)	MC8x series MC9x series
ARM (32bit high performance MCU)	AC3x series
Others	The others

## Revision history

Version	Date	Device group				Description
		ADAM & SRC	8051 & 800	ARM	Other	
V1.093.00			○			A96S174 added, 2021-02-26 Fine chip 8bit The info. of checksum and number of write is written in the option-area. Display of Hardware checksum is added.
V1.092.00	2021.02.19			○		AC33M3064/4064: Flashloader updated, Boot pin high enabled
V1.091.00	2020.12.24		○	○		A33G524/526/527 : tool option added, erase data flash Device name change : 97F8324 => MCF8014
V1.090.00	2020.11.20		○	○		Add Set Target VDD Dialog E-PGM serial devices added - 96R136, 96R150 - 81F4104, 81F4204, 81F4315, 81F4316, 81F4432 - 95FG0128_ISP, 96FC864A, 96FC664A, 96F4548 80F0448, 7708 UART : VDD 5.0 -> 4.5
V1.080.00	2020.05.20		○	○		Add : A96G166, A33G524, A96G148

V1.070.00	2019.12.16					Homepage-address is changed for download button. MC95FG0128 Vdd check change 10% => 20% 94BXXXX, mode entry for Vdd is changed 4ms->2ms
V1.060.00	2019.12.03					External Power Check is modified. DIR_H(CTL2) is cleared at power-off
V1.051.00	2019.11.11		O	O		A96G166 added A31GXXX, A33GXXX, AC33MXXXX : Vdd change from 3.3V to 4.5V when devices are In reset-state at ISP. 96FXXXX A and S series : Write checksum-info at option area(0x1B~1C, 0x3B~3C) m320 added
V1.050.00	2019.09.06		O	O	O	E-PGM Serial added A31GXXX, A33GXXX, AC33MXXXX, 96FXXXX, 96R7XX, 97FXXXX (EP_SA_ABOV_32.hex, EP_SA_FINE_8.hex) A94B428 added 97F1204S_5V added(5.0V target only)
V1.040.00	2019.06.20		O	O		E-PGM Serial integrated Vdd is changeable Write limit : hex encryption A31G123 - hex file with user0 supported - hex file with change GPIO from SWD supported A94B316 - serial id supported
V1.034.00	2019.03.19			O		A33M418 added AC33M4064 / 8128 fixed : AIRCR : SYSRESET => VECTRESET Vdd : 4.5V => 3.3
V1.033.00	2019.02.07		O	O		- Fix : A96R136, write protection, config read - flash loader update : A31G213, A31T414 A31R114, A31R118
V1.032.00	2018.11.20		O	O		-Add A96R150, A31G213, A31G111 A31R112, A31R114, A31R118 - E-PGM-Serial support

V1.031.10	2018.09.14			O		<ul style="list-style-type: none"> <li>- A31G112/123</li> <li>=&gt; Option display by word size</li> <li>=&gt; Support when reset pin is not connected</li> </ul>
V1.031.00	2018.07.25			O		<ul style="list-style-type: none"> <li>-Fix : 32 bit SWD</li> <li>- AIRCR : VECTRESET =&gt; SYSRESET</li> </ul>
V1.030.00	2018.05.03		O	O		<ul style="list-style-type: none"> <li>-Add</li> <li>AC33M4064_SWD, AC33M3064_SWD</li> <li>MC96F4548, A96R136</li> <li>A94E114, MC96FE316</li> <li>-Fix : A96T218, Option write error fixed when locked</li> </ul>
V1.029.00	2018.03.08		O	O		<ul style="list-style-type: none"> <li>- Add MC96FT241</li> <li>- Fix A96T316/326/336/346, A96T218/228</li> <li>disable external reset</li> <li>- Fix A96R717/739 : user selection for irc-trim</li> </ul>
V1.028.00	2017.11.16		O	O		<ul style="list-style-type: none"> <li>- Add A31G123, MC96F8168TS</li> <li>- Fix MC95FG308_ISP : display checksum by 16bit</li> </ul>
V1.027.00	2017.08.28		O			<ul style="list-style-type: none"> <li>- Vdd and run-flag are off at the same time.</li> <li>- Avoid user-mode run at Power-off.</li> <li>- MC97F60128/66128</li> <li>- MC97F2664/2464</li> </ul>
V1.026.10	2017.08.04			O		<ul style="list-style-type: none"> <li>- reset voltage trim : 4.3V =&gt; 3.3V</li> <li>- device : AC33M8128_SWD, AC33M4064_SWD</li> <li>AC30M1464_SWD, AC30M1364</li> </ul>
V1.026.00	2017.07.24		O			<ul style="list-style-type: none"> <li>- added MCU stop command in Power-off</li> <li>- Avoid user-mode run at Power-off</li> <li>- related devices are <ul style="list-style-type: none"> <li>- MC96F6432/6332/6432S/6432A</li> <li>- MC96F8316/8216/8316S/8316A/8208S</li> <li>- MC96F6832/6632</li> <li>- MC96F7816/7616</li> <li>- MC96F6508A/6409</li> <li>- MC97F60128/66128</li> <li>- MC97F2664/2464</li> </ul> </li> </ul>
V1.025.00	2017.05.29		O			<ul style="list-style-type: none"> <li>- REGDATA check for noise and poor contact</li> <li>- Decrease abnormal trim erase/write</li> <li>- MC96F6432/6332/6432S/6432A</li> <li>- MC96F8316/8216/8316S/8316A/8208S</li> <li>- MC96F6832/6632</li> <li>- MC96F7816/7616</li> <li>- MC96F7864/7664</li> <li>- MC96F6508A/6409</li> <li>- MC97F60128/66128</li> </ul>

						- MC97F2664/2464
V1.024.00	2017.05.15		O			Add : A97C450 Add : Offline Losd/Save HPO
V1.023.02	2017.02.01	O				ADAM26P20 : change power control, add delay 400ms ADAM28P16/29P16 : remove verify 3/4
V1.023.01	2017.01.24		O			MC97F1316S : Vpp change from 17.0V to 16.5V
V1.023.00	2017.01.06		O			Fix : MC81F4 Series, MC81F6104/6204 Preprogram MC96F1206 : COB add A96T316 : ADC version add ADAM22PXX : added
V1.022.00	2016.12.22		O	O		Add : A31G112, A96T326, A96T228 Fix : AC30M1464, SWD Mode Entry, Use DebugPowerRequest
V1.021.00	2016.12.02	O				Fix : ADAM28P16/29P16, write time enhanced Poor contact fixed
V1.020.00	2016.11.02	O	O			Add : A96T336, A96R717 ADAM29P16 : write time 5.6 sec => 2.9 sec Vdd rise time 2=>4ms, for poor contact error Fix : HPO file failed if no device option Add : Option length over 20
V1.019.00	2016.09.19	O	O			Fix : ADAM28P16, ADAM29P16 Add : ADAM45P5216, ADAM45P5217, ADAM45P6216, ADAM45P6217 Add : A96T316, A96T218, A94F1202A_WAFER, MC80F7708A
V1.018.00	2016.08.18		O			Add : MC96F6632S Fix : On saving HPO, error is occurred if option is not selected. Add : DLL function is added
V1.017.01	2016.08.03					Remove : MC97F6208
V1.017.00	2016.07.22			O		Add : AC30M1464_SWD, AC30M1432_SWD
V1.016.00	2016.06.03		O			Add : 96FC864A/664A Add : 81F6104/81F6204 Add : 33M384A_SPI Add : 96FR364C_IRC - 96F6432S/8316S/8208S => checksum is displayed when device is locked => write-speed is improved by skipping blank page
V1.015.00	2016.04.26	O	O	O	O	Add : 96FR364C, AC33M8128_SWD, 96F8204, 96F8316A, 80F0504, 80F0604 Fix : Select 96F6409 =>LCD device : 96F8204 Add : 96F6332, 96F8216, 96FT1604, 96FT202

						ADAM46P2008 Firmware is modified(IRC out delay time)
V1.014.00	2016.01.22	O	O	O	O	Add : DLL function
V1.013.01	2015.12.31	O	O	O	O	Fix : FileName buffer size : 256 => 1024
V1.013.00	2015.12.11		O			Add : 96F6432A Fix : Moto. format HPO file save/load added
V1.012.00	2015.11.27	O		O		Add : 96F1206, 33GA256
V1.011.01	2015.11.17					Fix : 96FRXXXX, when write protection is enabled, Verify is failed. Add : 96F1206 Fix : 97F1204S, read lock checksum, vdd 2.5 => 3.6
V1.011.00	2015.10.19		O			Fix: 81F4104/4204/4205/4215/4315/4316/4416/4432/4432 Write check routine added before write-back-routine. Trim-erased-failure rate is decreased. 97F6108A/6208/8324 Trim(IRC) : F/T revised, Trim(0x01) : IRC trim Trim(0x1F) : 0x3C
V1.010.00	2015.10.07		O			Revision: 97F1104S/1204S - F/T ID : 0xF7 => 17V - F/T ID : 0xFC => 16.5V - PGM time : max 3.5ms x 4 - Verify : 3.8V & Hardware checksum
V1.009.00	2015.10.05	O				Add : ADAM series
V1.008.02	2015.10.02		O			Add - 96FR364B/96FR332B : support LAN socket * Level shifter LAN socket
New version rule is activated from 2015.10.02						
150804	2015.08.04		O			Fix: 96FT1616/FT242, DevicePowerOn() typo "return FALSE" => "return TRUE"
150803	2015.08.03	O	O	O	O	External Power check Before writing, if external Vdd is over 0.7V, "Ext. power error" is occurred.
150730	2015.07.30		O			Communication error fixed : Fine chips device SIO-RX(), SIO_TX() : Delay_us(1) add
150717	2015.07.17		O			- 97F8324 Vdd change : 5.0 => 4.5V, Clk/data pull-down resistor (4.7K) support
150714	2015.07.14		O			Add : Chinese / English selection Fix : 96FR364B/96FR364B-ISP end address : 0x7fff => 0xffff
150616	2015.06.16		O			Fix : - 97F60128 / 2664 : remove Trimming Vpp routine : Vpp Cap. 4.7uF, Power fail.

						- 96FR116C no operation : firmware bug
150605	2015.05.21		O			Add - 96F6832 / 6632 / 7816 / 7616T / 7616A / 7416A - 96F7848C
150429	2015.04.28		O			Add - 96FG316, 96FD316 - 96F6432/6332/8316/8216, speed-up, S-PGM+ : 8 sec., E-PGM+ : 4 sec.
150429	2015.04.28	O	O			Add - 40P5X04B - 95FG208/308 serial/parallel - 96FR332B/364B serial/parallel - 96FR3128/4128 serial/parallel Fix - 97F60128 bug : option write fail
150422	2015.03.11		O			Add 81F4XXX series
150113	2015.01.13		O			97F1204S, trim-write-back minimize
140725	2014.07.25		O			Fine chips device : second write removed Fix Page buffer fault
140612	2014.06.12					Add USB power off routine Fix : USB connection error
140609	2014.06.09		O			First release

## Available device list

Device series	Devices
ADAM & SRC	ADAM24P16, ADAM24P16S, ADAM24P20 ADAM27P08, ADAM27P16 ADAM28P16, ADAM29P16 ADAM41P2723, ADAM41P2727 ADAM42P11, ADAM43P1108, , ADAM46P20 ADAM82P1216, ADAM82P1224 ADAM85F2316 MC40P5004B / 40P5104B / 40P5204B / 40P5304B / 40P5X04CB
8051 & 800	81F4104/4204/4205/4215/4315/4316/4416/4432/4432 95FG208/95FG308 96F5304 96F6409 96F6432 / 96F6332 96F8316 / 96F8216 96F6432S / 96F6332S 96F8316S / 96F8216S / 96F8208S

96F6408A/96F6508A  
96F6632/96F6832  
96F7416A/96F7616A  
96F7616T/96F7816  
96F7664/96F7864  
96F7848C  
96FD316  
96FR116C, 96FR332B/96FR364B, 96FR4128/96FR3128  
96FT1616, 96FT1604,  
96FT241, 96FT242, 96FT202  
97F1104S/97F1204S, 97F1316S  
97F2208, 97F6108A, 97F6208, 97F8324  
97F2464/97F2664  
97F60128  
97FG316,  
96F8316A, 96F6432A  
96F8204  
80F0604, 80F0504, 80F0708  
96FC864A / 96FC664A  
81F6204 / 81F6104  
80F0448  
80F1604  
80F7708  
80F7708A  
A94B114  
A94B316  
A94F1202A  
MC95FG0128-ISP  
MC96FR364C  
96F1206, 96F4548, 96F5216  
96F7848C  
96L302, 96L312  
96R136, 96R150, 96R717, 96R725, 96R739  
96T218, 96T228, 96T316, 96T336, 96T346, 96T356  
97C450, 97C624  
96G140, 96G166  
94B428  
94B336

ARM	33MA384A AC33M4064- SPI / UART / SWD AC33M8128-SPI / UART / SWD 30M1432 / 30M1464 A31G111, A31G112, A31G123 A31G213 A31G313, A31G314, A31G316 A31R112, A31R114, A31R118 A33G526, A33G527 AC33GA256-SPI A34M418 m320 A31T414
Other	AM3111 MF6216